

106080-00482860

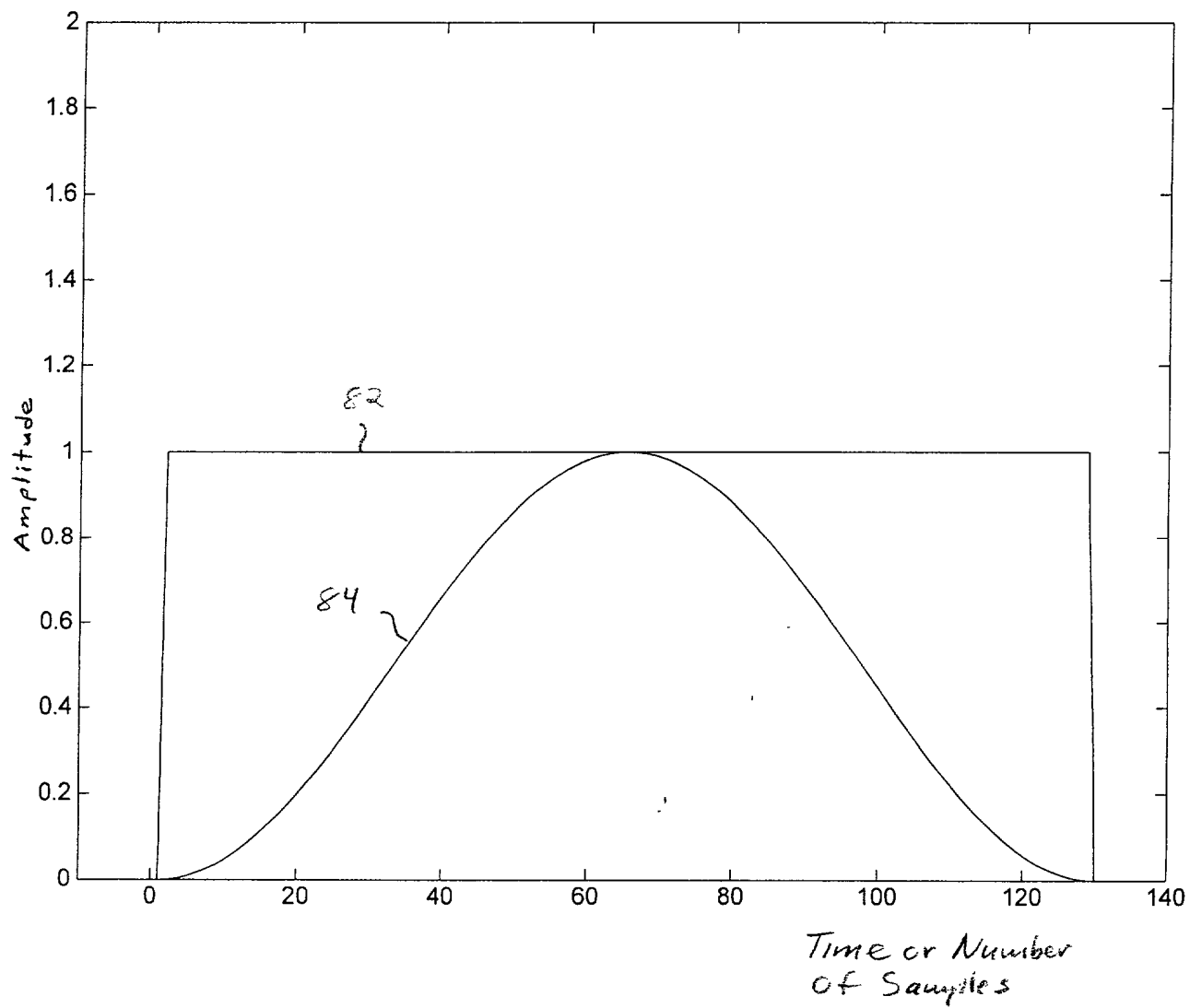


FIG. 5

Diagram illustrating a 3D memory layout with three frames and their corresponding data blocks:

- Frame #1** (Top): Contains data blocks 1, 128, 129, 256, 257, and 384. The frame is bounded by 70 and 72.
- Frame #2** (Middle): Contains data blocks 65, 192, 193, and 320. The frame is bounded by 71 and 73.
- Frame #4** (Bottom): Contains data blocks 71 and 73. The frame is bounded by 70 and 72.

A hand-drawn graph showing Average Power versus Frequency. The graph features a dashed curve with several peaks and valleys. Vertical lines connect the curve to the horizontal axis at various points. Horizontal lines are drawn across the graph at different power levels. The number '92' is written near a peak, and '94' is written near a valley. The y-axis is labeled 'Average Power' and the x-axis is labeled 'Frequency'.

FIG. 6

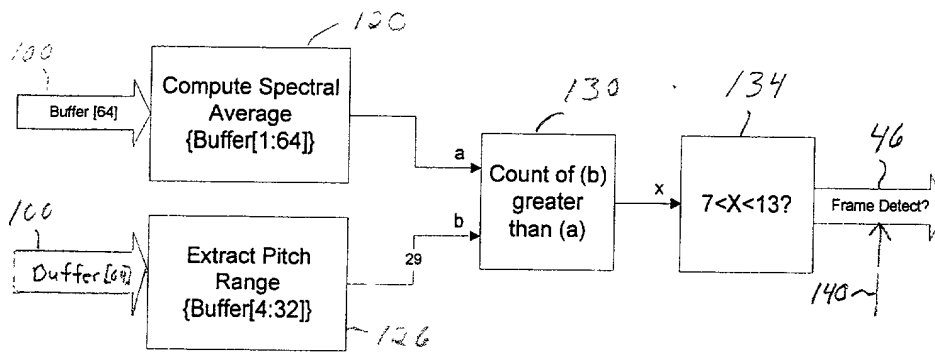


FIG. 7

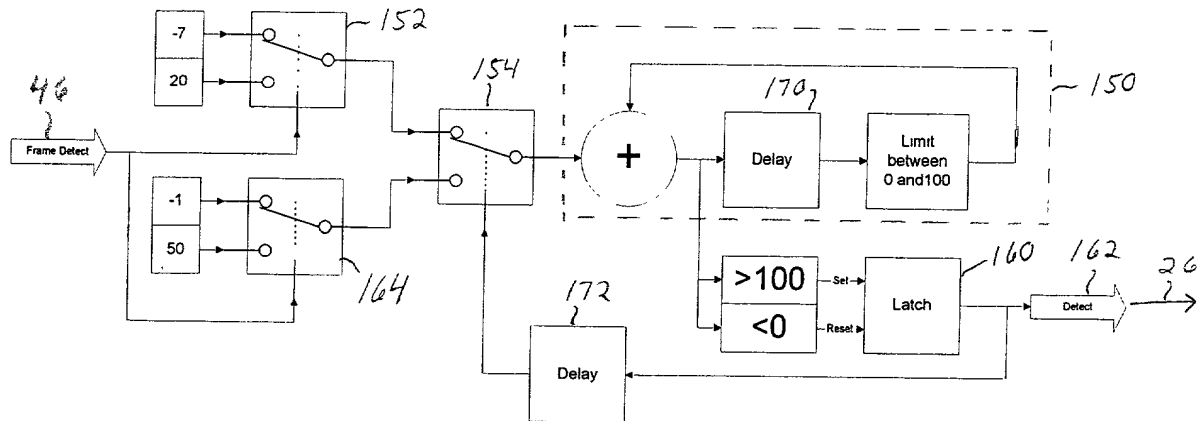


FIG. 8

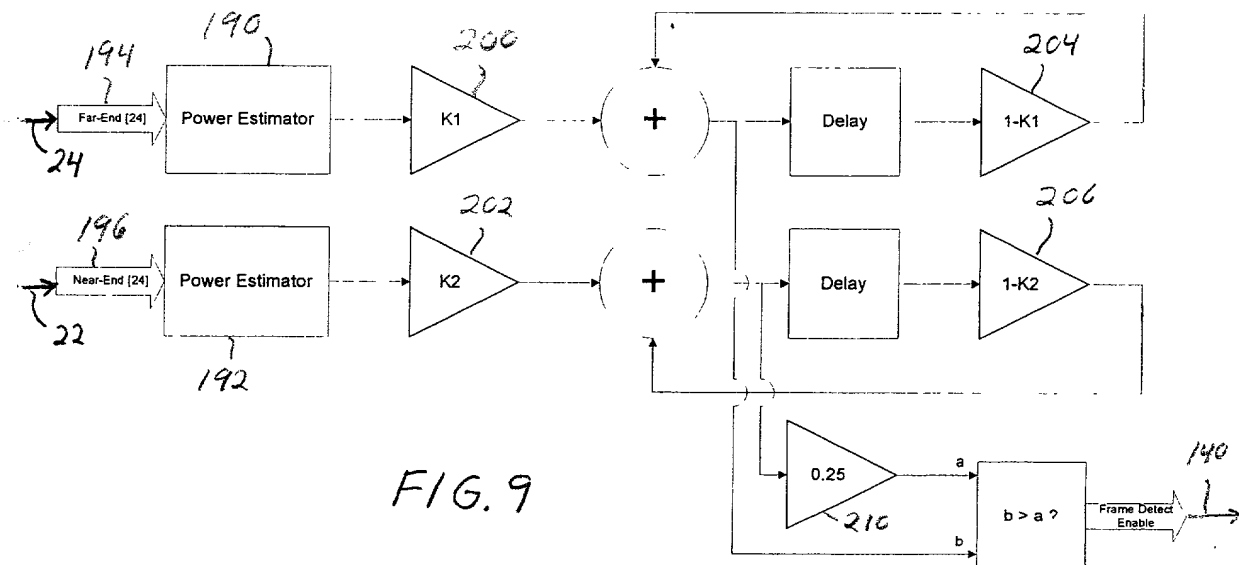


FIG. 9